

<b>Notice of Allowability</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	09/632,373	VOLOSHIN, MOSHE	
	<b>Examiner</b>	<b>Art Unit</b>	
	Phuongchau Ba Nguyen	2665	

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 10-20-4 Terminal Disclaimer & response.
2. ☒ The allowed claim(s) is/are 10-12,14-15; Renumbered as 1-5 respectively.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☐ All    b) ☐ Some\*    c) ☐ None    of the:
  1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  5. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
    - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
      - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
    - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

- |   |  |
|---|--|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892)  | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)            |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                | 6. <input type="checkbox"/> Interview Summary (PTO-413),<br>Paper No./Mail Date _____. |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),<br>Paper No./Mail Date _____ | 7. <input type="checkbox"/> Examiner's Amendment/Comment                               |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit<br>of Biological Material          | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance   |
|   | 9. <input type="checkbox"/> Other _____.   |

### REASONS FOR ALLOWANCE

1. The following is an examiner's statement of reasons for allowance:

Regarding claim 10, the prior art fails to teach an apparatus for automatically activating a clock master circuit in a stack of repeaters comprising a second repeater comprising "a clock master circuit that is enabled when the first on pin logical state indicates that no other repeater occupying a position in the repeater stack that is before the position of the second repeater is powered on; and a connector connecting the first on pin from the second repeater to the power state output pin of the first repeater," which is considered in combination with other limitations, as specified in independent claim 10.

Regarding claim 11, the prior art fails to teach an apparatus for automatically activating a clock master circuit in a stack of repeaters comprising a second repeater comprising "means for generating a master clock signal, said means for generating comprising mean for enabling said means for generating, said means for enabling being responsive to the means for determining," which is considered in combination with other limitations, as specified in independent claim 11.

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Regarding claim 12, the prior art fails to teach an apparatus for automatically activating a clock master circuit in a repeater comprising “a clock master circuit having an enable input wherein the enable input is controlled by the first on pin logical state, whereby the clock master circuit is enabled according to the state of the local first on pin and the local first on pin is pulled by the voltage to a first logical state unless the local first on pin is connected to a remote power state output pin that pulls the local first on pin to the second logical state,” which is considered in combination with other limitations, as specified in independent claim 12.

Regarding claim 14, the prior art fails to teach a method of automatically activating a clock master circuit in a stack of repeaters comprising “connecting the first on pin in the first repeater to a power state output pin in a second repeater wherein the power state output pin is configured to be connected to ground when the second repeater is powered on; and enabling a clock master circuit having an enable input based on the voltage on the first on pin,” which is considered in combination with other limitations, as specified in independent claim 14.

Regarding claim 15, the prior art fails to teach a method of automatically activating a clock master circuit in a stack of Fast Ethernet repeaters comprising “a step for connecting the first on pin in the first repeater to ground if a second repeater is powered on; and a step for enabling a clock master circuit based on the voltage on the first on pin,” which is considered in combination with other limitations, as specified in independent claim 15.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled “Comments on Statement of Reasons for Allowance.”

2. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phuongchau Ba Nguyen whose telephone number is 571-272-3148. The examiner can normally be reached on Monday-Friday from 10:00 a.m. to 2:00 p.m..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Huy Vu can be reached on 571-272-3155. The fax

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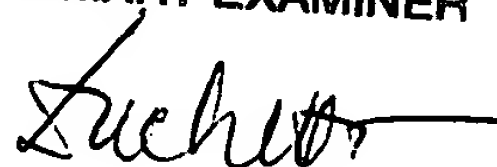
phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Phuongchau Ba Nguyen  
Examiner  
Art Unit 2665

DUCHO  
PRIMARY EXAMINER



12-12-05